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General Comment

For analog to digital system upgrades the analog signals received by digital processor; filtered, digitized, manipulated, converted back into analog form, filtered again, sent out for safety-related purposes. The associated filter component reduces aliasing noise introduced by signal frequencies high relative to fixed sampling rate and the amplitude of signal is held long enough to permit conversion to a digital word. For a sampling rate higher than twice analog signal bandwidth, then the sampled signal is a good representation of analog input signal. Analog signals containing frequencies too high versus the sampling rate, aliasing uncertainty will be introduced. Either anti-aliasing band limiting filters should be used to minimize aliasing uncertainty or this error should be accounted for in setpoint calculations. The draft regulatory guide should address this type of error.