

MELTAC Life Cycle Document Schedule

Non-Proprietary

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Revision History

Revision	Date	Page (section)	Description
0	June 2016	All	Initial issue
1	September 2016	P2 - 4	Added the following document types. <ul style="list-style-type: none">- Configuration Management- Requirement Traceability Matrix- FMEA- Risk Matrix- Drawing
		P2, 4	For the following modules, changed the schedule of EQ Test Plan, WQ Test Spec, EQ Test Report and V&V Final Report from "2nd" to "3rd". <ul style="list-style-type: none">- FMU Module- E/O Converter Modules- Safety VDU Panel
		P2 - 4	Added the NOTE for V&V activity about the modules that do not include FPGAs. (NOTE is added in P4.)

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1.0 INTRODUCTION

This document describes the schedule to disclose to the NRC the life cycle documents associated with the Mitsubishi Electric Corporation (MELCO) Energy Systems Center (ESC) Mitsubishi Electric Total Advanced Controller (MELTAC) Platform. The development of modules (module design, V&V and EQ test activities) occur in phases. The schedule encompasses the MELTAC Platform hardware and the basic software, which includes the firmware and Field Programmable Gate Arrays (FPGAs) on all MELTAC Platform modules.

This document supports the “Safety System Digital Platform - MELTAC - Topical Report” (JEXU-1041-1008).

2.0 DISCLOSURE SCHEDULE

Table 1 shows the schedule to disclose the life cycle documents.

Table 1. MELTAC Life Cycle Document Schedule

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