Enclosure 2

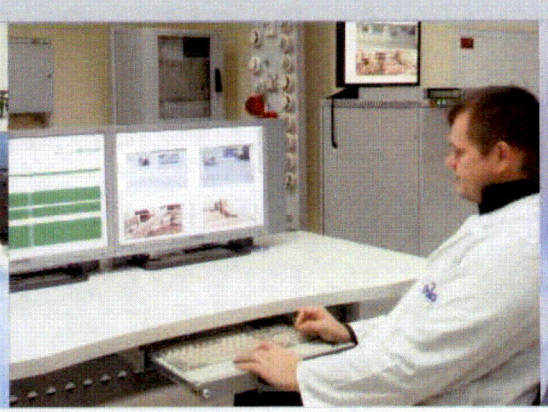
Non-Proprietary Presentation Material for July 14, 2015 Meeting

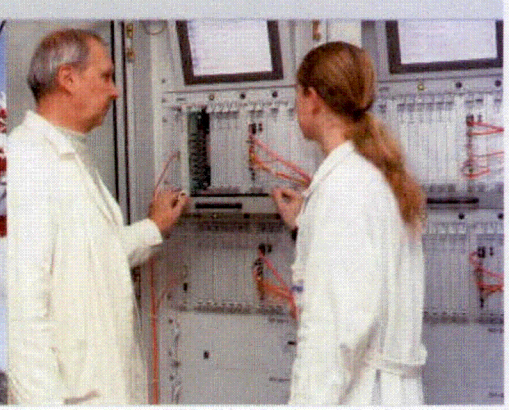
RadICS Digital I&C Platform Topical Report

Overview of Radiy and RadICS Topical Report

Phase 0 Pre-Application Meeting (Open Session)

July 14, 2015, Rockville, Maryland



Meeting Purpose

- Radiy plans to submit to NRC for review and approval
- Purpose of meeting is to present:
  - Technical information about the RadICS platform
  - Planning Information about RadICS Topical Report submittal
- Meeting will be presented in two part:
  - Open Session – General overview of Radiy and RadICS
  - Closed Session – Proprietary information about the RadICS platform and topical report submittal

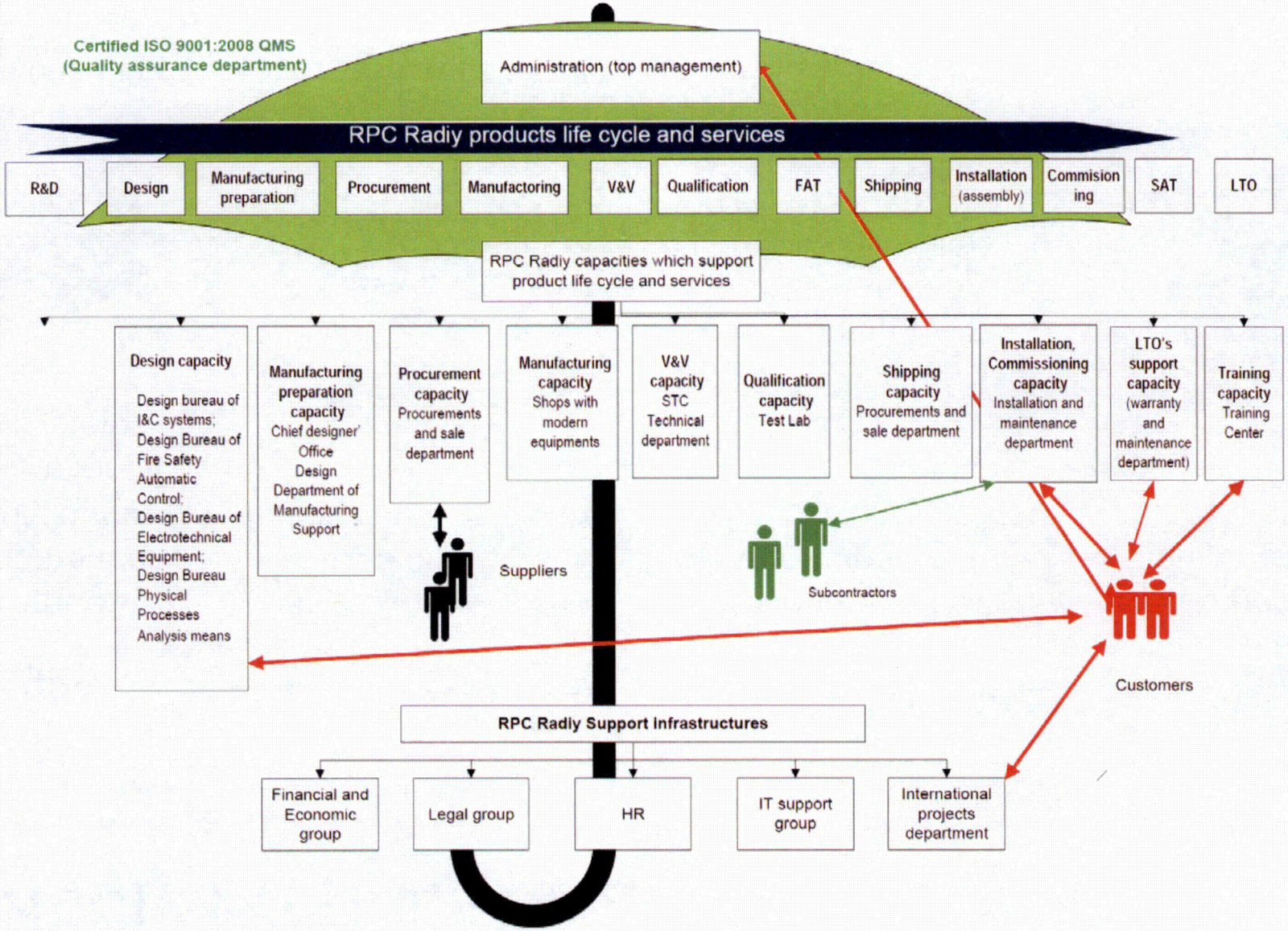
Agenda

- Meeting Purpose
- About “Radiy”
- Nuclear Organization
- Products for Nuclear Power Plants
- Manufacturing and Qualification Test Facility
- Product/Project Experience
- Meeting Purpose
- Expected Outcomes

About "Radiy"

- 920 employees, 200 engineers, headquartered in Kirovograd, Ukraine
  - 20 years servicing Ukrainian NPP industry
  - 17 years providing FPGA-based systems to Ukrainian NPP industry
  - 7 years providing FPGA-based systems to Bulgarian NPP industry
- Annual turnover: 100 million Euros
- Main profile: FPGA-based I&C systems for NPPs
- All in-house processes: design, procurement, manufacturing, testing, installation

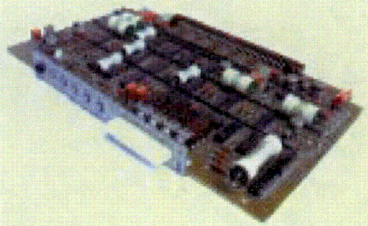
Nuclear Organization



Radiy Product Evolution

1995

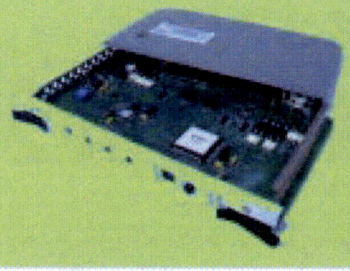
Started development and supply of the equipment for NPP I&C systems



Replacement of obsolete NPP I&C modules

1998

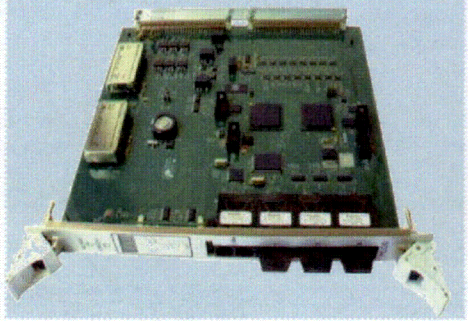
First generation of equipment for NPP I&C systems



FPGA-based I&C systems for NPP

2002

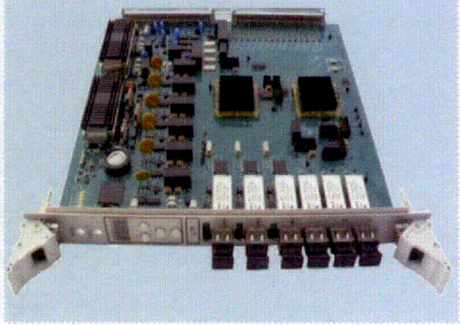
Second generation of equipment for NPP I&C systems



FPGA-based I&C platform for NPP

2014

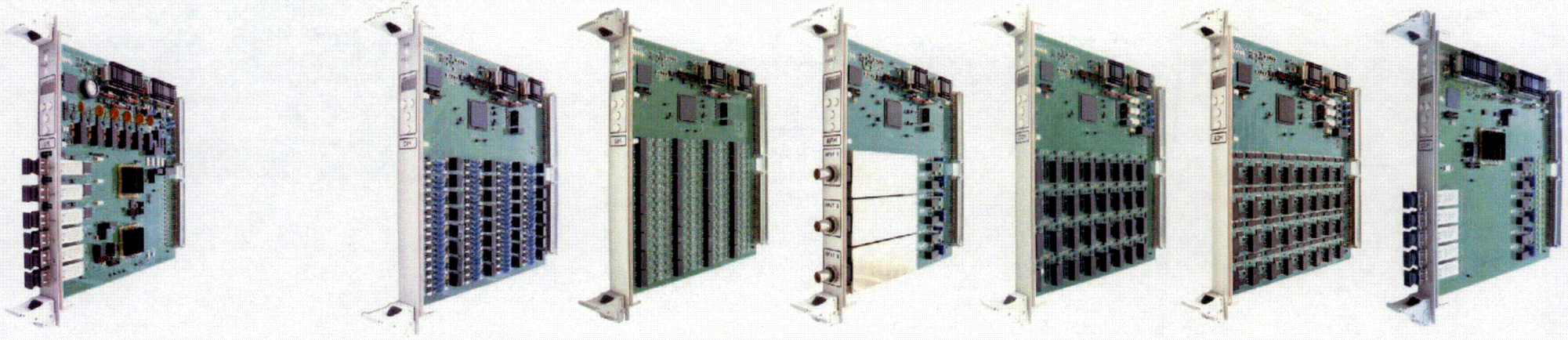
Third generation of equipment for NPP I&C systems



SIL3 certified FPGA-based I&C platform for NPP

FPGA-based Universal I&C Platform

RadICS™ | Comprising Modules



Logic Module (LM)

- Dedicated FPGA chip for user configurable control logic
- Logical and physical separation of control logic and system function
- Integrity checks on each communication line (CRC)
- 14 LVDS full duplex lines for communication with OCM and I/O modules
- 2 LVDS simplex/duplex lines for diagnostic purposes
- 3 galvanic-isolated discrete inputs
- 3 fiber optical lines for internal system communications
- 1 input for Tuning PC programming access key signal
- 3 Fast Ethernet (100 BASE-FX) optical communication lines
- Hot swappable

Discrete Input Module (DIM)

- 32 independent input discrete channels ("dry" contact type)
- Enhanced inputs diagnostics
- Integrity checks on each communication line (CRC)
- 2 LVDS lines (diagnostic and information)
- Hot swappable

Analog Input Module (AIM)

- Enhanced I/O diagnostics
- 32 independent analog input channels
- 16-bit A/D conversion in each analog input channel
- 2 LVDS full duplex lines (diagnostic and information)
- Integrity checks on each communication line (CRC)
- Built-in calibration channel
- Hot swappable

Analog Input for Neutron Flux Measurement Module (AIFM)

- 3 high-sensitive independent galvanic-isolated analog input channels with counting, cambelling or current mode
- Enhanced I/O diagnostics
- 3 analog output channels with linear or logarithmic D/A conversion
- 2 LVDS lines (diagnostic and information)
- Integrity checks on each communication line (CRC)

Discrete Output Module (DOM)

- 32 independent digital form-A optic-relay isolated output channels (switching up to 48 V DC / 0.5 A)
- 2 LVDS lines (diagnostic and information)
- Integrity checks on each communication line (CRC)
- Enhanced active output diagnostics
- Fuse and Overvoltage protected outputs
- Hot swappable

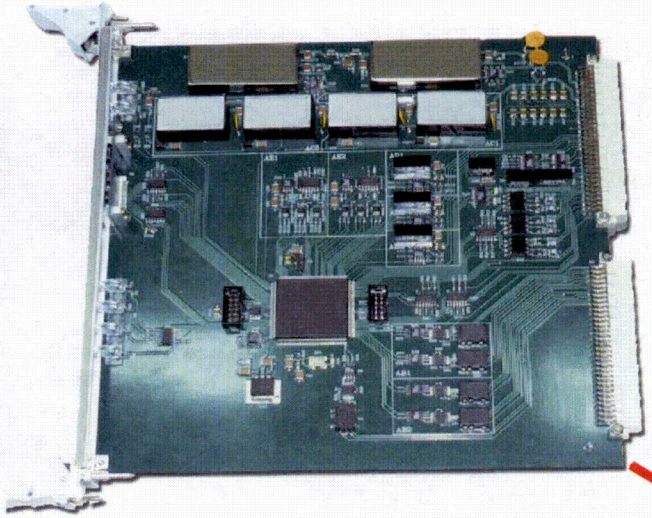
Analog Output Module (AOM)

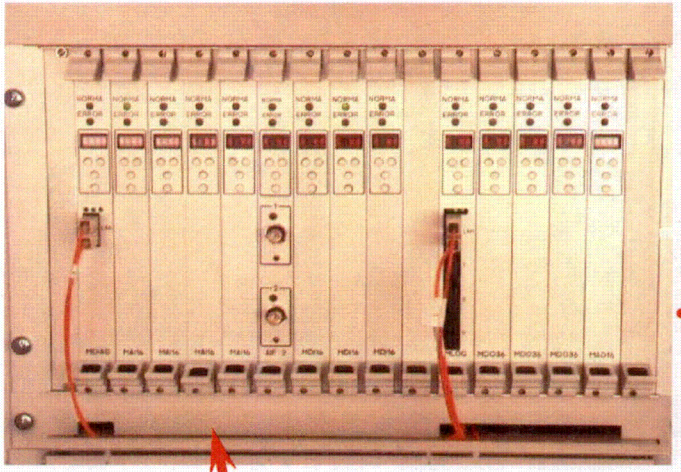
- 32 independent output channels
- 16 bit D/A conversion in each channel
- Enhanced diagnostics of output current channels
- 2 LVDS lines (diagnostic and information)
- Integrity checks on each communication line (CRC)
- Hot swappable

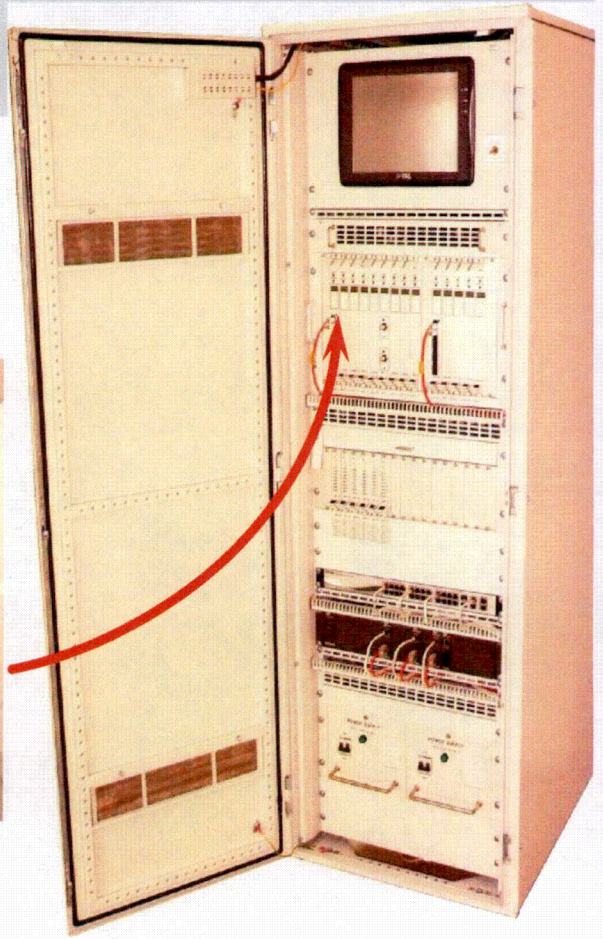
Optical Communication Module (OCM)

- 5 fiber optical lines
- 2 LVDS lines (diagnostic and information)
- Integrity checks on each communication line (CRC)
- Hot swappable
- 5 RS-232 or RS-485 serial communication interfaces

RadICS Platform Equipment

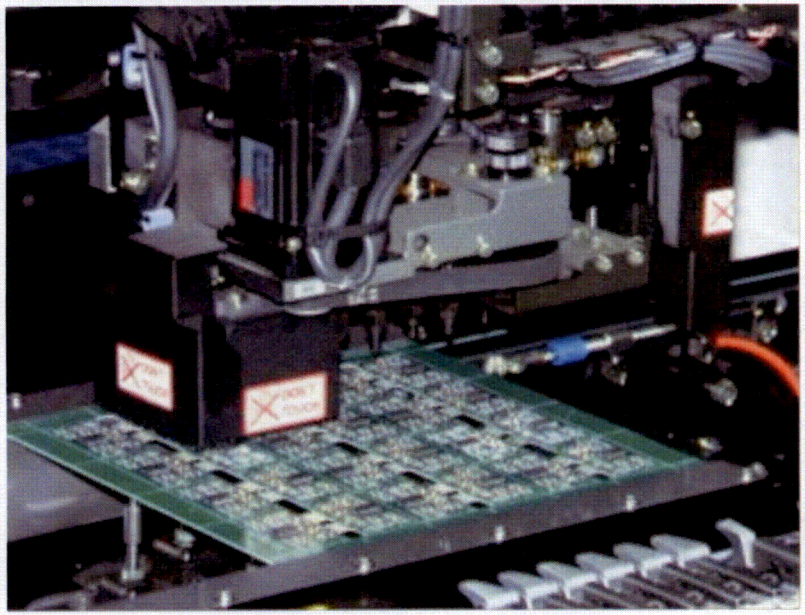






Manufacturing Test Facility

- Manufacturing and inspection facilities comply with Company Quality Management System (QMS) based on ISO, IEC, and IPC Standards



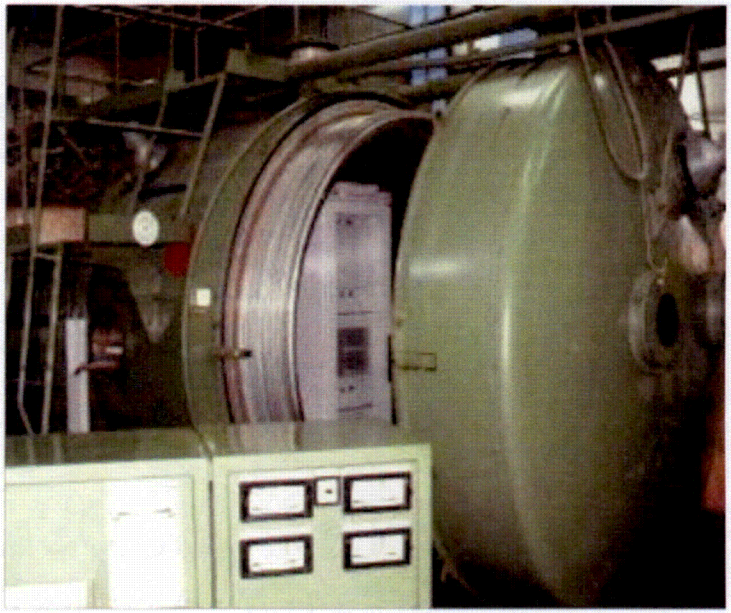
Automated production line for PCBs surface mounting

Automated sheet shearing machine AMADA

Qualification Test Laboratory

- Radiy Qualification Test Facility certified to ISO/IEC 17025:2005
  - Environmental and Seismic Capabilities

Electrodynamic Vibration Table V875-440 HBT Combo, LVD

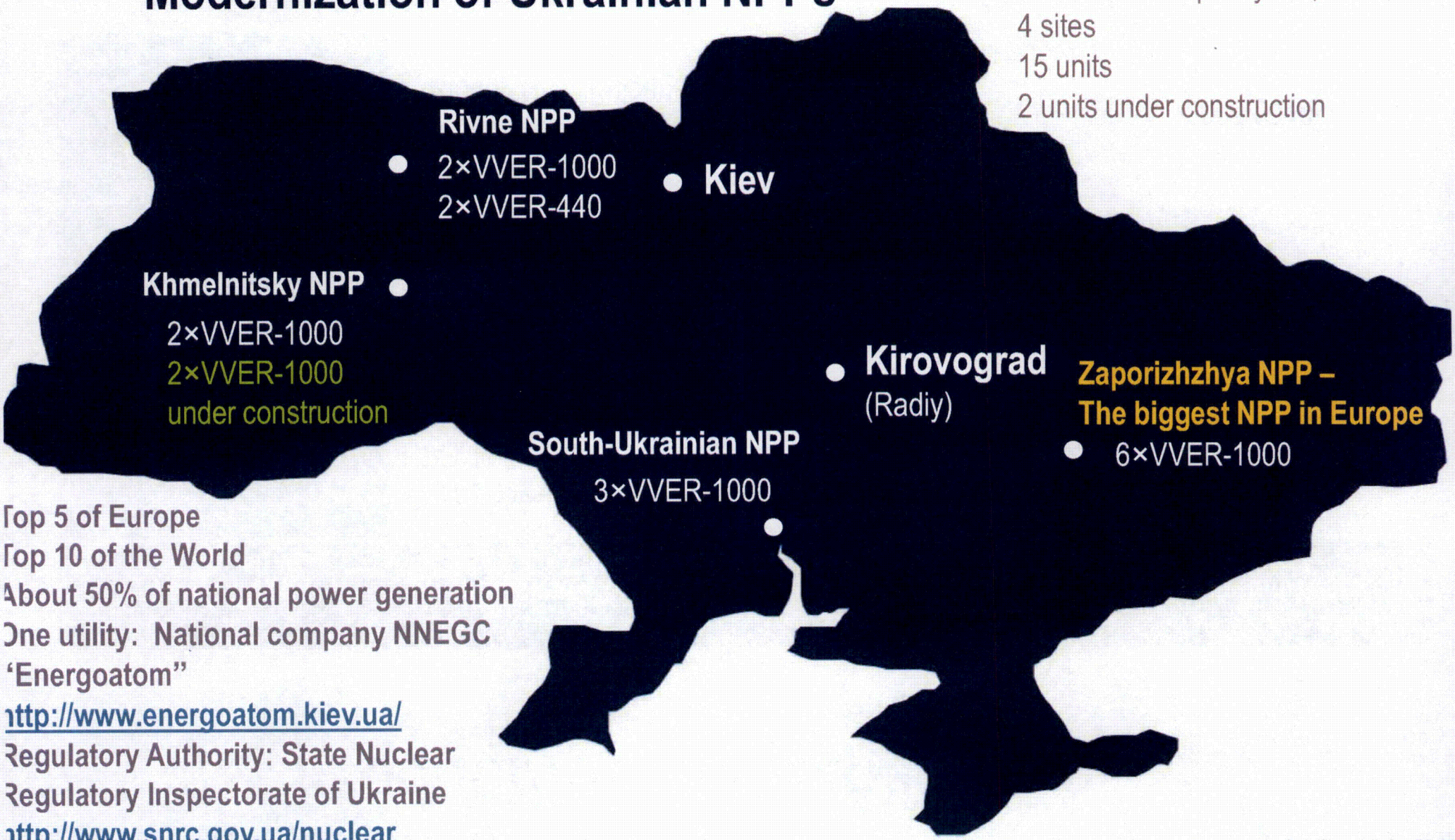


Climatic thermal pressure chamber KTBV 8 1

- US test laboratory will be used for RadICS Topical Report Testing

Modernization of Ukrainian NPPs

Total Gross Capacity: 13,835 MW
4 sites
15 units
2 units under construction

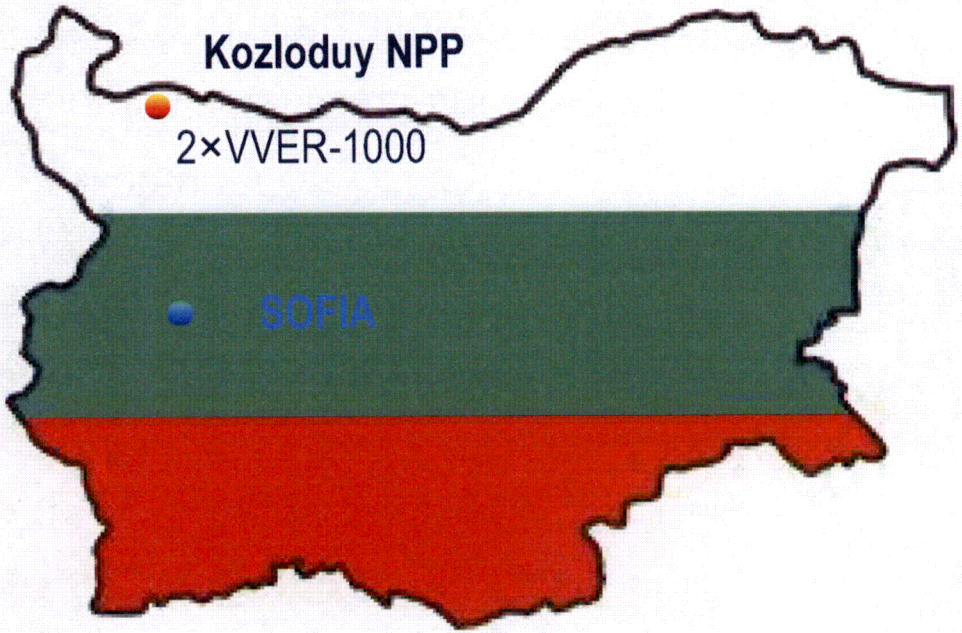


Top 5 of Europe
Top 10 of the World
About 50% of national power generation
One utility: National company NNEGC "Energoatom"
http://www.energoatom.kiev.ua/
Regulatory Authority: State Nuclear Regulatory Inspectorate of Ukraine
http://www.snrc.gov.ua/nuclear

Product/Project Experience

Systems Supplied	Nuclear Power Plant	Number of Installed Systems	Installation Years
Reactor Trip System	Zaporozhye NPP; South-Ukraine NPP; Rivne NPP; Khmelnitski NPP	30	2004-2014
Reactor Power Control and Limitation System	Zaporozhye NPP; South-Ukraine NPP; Rivne NPP; Khmelnitski NPP	10	2004-2012
Engineered Safety Feature Actuation System	South-Ukraine NPP, Rivne NPP, Kozloduy NPP, Bulgaria	18	2005-2010

Modernization Project for Kozloduy



- Modernization of 2 sets of Power Supply equipment for Rod Control System for Units 5,6 (2007– 2008)
- Modernization of 6 Engineering Safety Actuation Systems (ESFAS) for Units 5,6 (2008-2010)
- Modernization of 10 switchgears sets (RTZO cabinets) of ESFASs and of Nuclear and Conventional Island Control Systems for Units 5,6 (2013 – 2015)

Modernization Project for Kozloduy

- Increase safety of the NPP
- Increase NPP availability
- Assure long-term operation ability
- Improve human-machine interface for control, diagnostic and maintenance
- Improve of electrical and physical separation between safety divisions
- Assure lifetime service and maintenance
- Comply with regulatory requirements
- Assure minimization of on-site premises reconfiguration

exida CERTIFICATION

The manufacturer may use the mark:

Valid until October 1, 2017
Revision 1.0 September 26, 2014

ANSI Accredited Program
PRODUCT CERTIFICATION
#1004

Certificate / Certificat
Zertifikat / 合格証

RAD 1406037 C001

exida hereby confirms that the:

FPGA-Based Safety Controller (FSC) RadICS
produced by **RPC Radiy**
29 Geroyiv Stalingrada Street
Kirovograd, Ukraine

Has been assessed per the relevant requirements of:

IEC 61508 : 2010 Parts 1-7

and meets requirements providing a level of integrity to:

Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type B Element

SIL 3 @ HFT = 0; Route 1_H

PFD_{AVG} and Architecture Constraints must be verified for each application

Safety Function:
The FSC will read input signals, perform user-defined application layer logic and write results to the output signals within the stated response time.

Application Restrictions:
The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements.

Evaluating Assessor

Rudolf P. Chalupka
Certifying Assessor

Page 1 of 2

FPGA-Based Safety Controller (FSC) RadICS

exida CERTIFICATION
64 N Main St
Sellersville, PA 18960

T-002, V3R4-3

Certificate / Certificat / Zertifikat / 合格証

RAD 1406037 C001

Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type B Element

SIL 3 @ HFT=0; Route 1_H

PFD_{AVG} and Architecture Constraints must be verified for each application

Systematic Capability :

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.

Random Capability:

The SIL limit imposed by the Architectural Constraints must be met for each element.

SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF must be verified via a calculation of average Probability of Failure on Demand (PFD_{AVG}), or Probability of Failure per hour (PFH), considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each subsystem must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

The following documents are a mandatory part of certification:

Assessment Report: RAD 14-06-037 R002 V1R0 61508 Assessment - FSC

Safety Manual: D11.1 - Radiy FSC Product Safety Manual V1R2

SIL3 in single channel configuration

Note: IEC SIL is different than IEEE Std 1012 SIL

Page 2 of 2

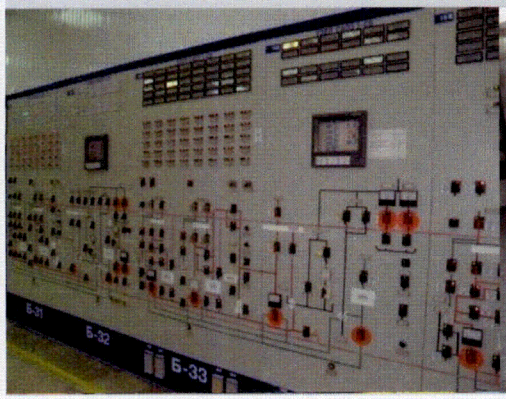
Expected Outcomes

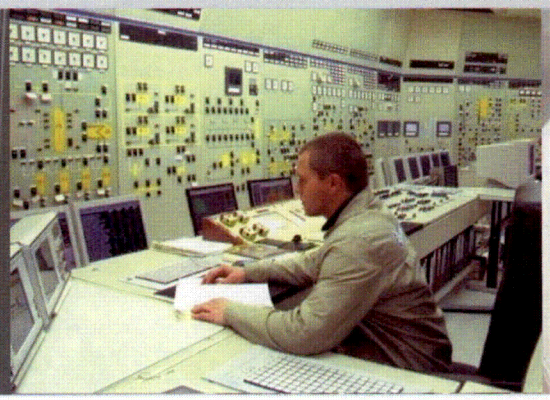
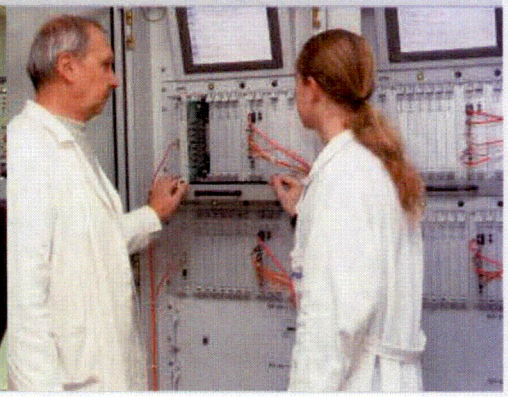
- Closed session will cover the following topics:
  - RadICS Digital I&C Platform
  - RadICS Development Processes
  - RadICS Quality Management System
  - RadICS Qualification Test Plan
  - Commercial Grade Dedication Plan
  - Details of RadICS Licensing Program
- Radiy would like NRC feedback on RadICS Platform features and understanding of NRC licensing requirements
- Radiy would also like NRC feedback on the overall licensing plan and schedule

radiy
RESEARCH & PRODUCTION CORPORATION

Thank you for your attention!

Research & Production Corporation Radiy
29, Geroyiv Stalingrada Street, Kirovograd 25006, Ukraine
e-mail: v.sklyar@radiy.com
http://www.radiy.com



Non-Proprietary

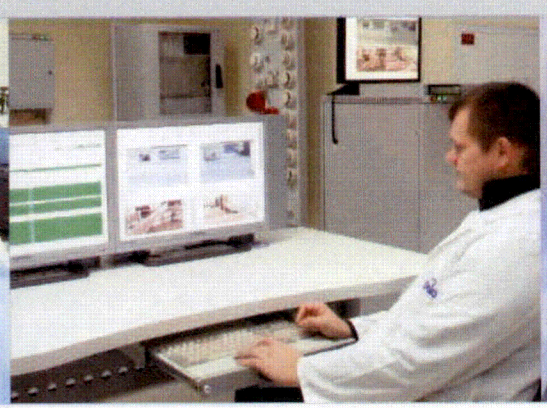
radiy
RESEARCH & PRODUCTION CORPORATION

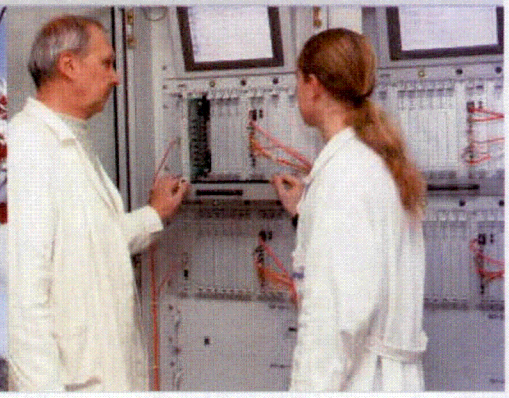
Technical Part 1: RadICS Digital I&C Platform Topical Report

RadICS Digital I&C Platform

(Closed Session)

July 14, 2015, Rockville, Maryland



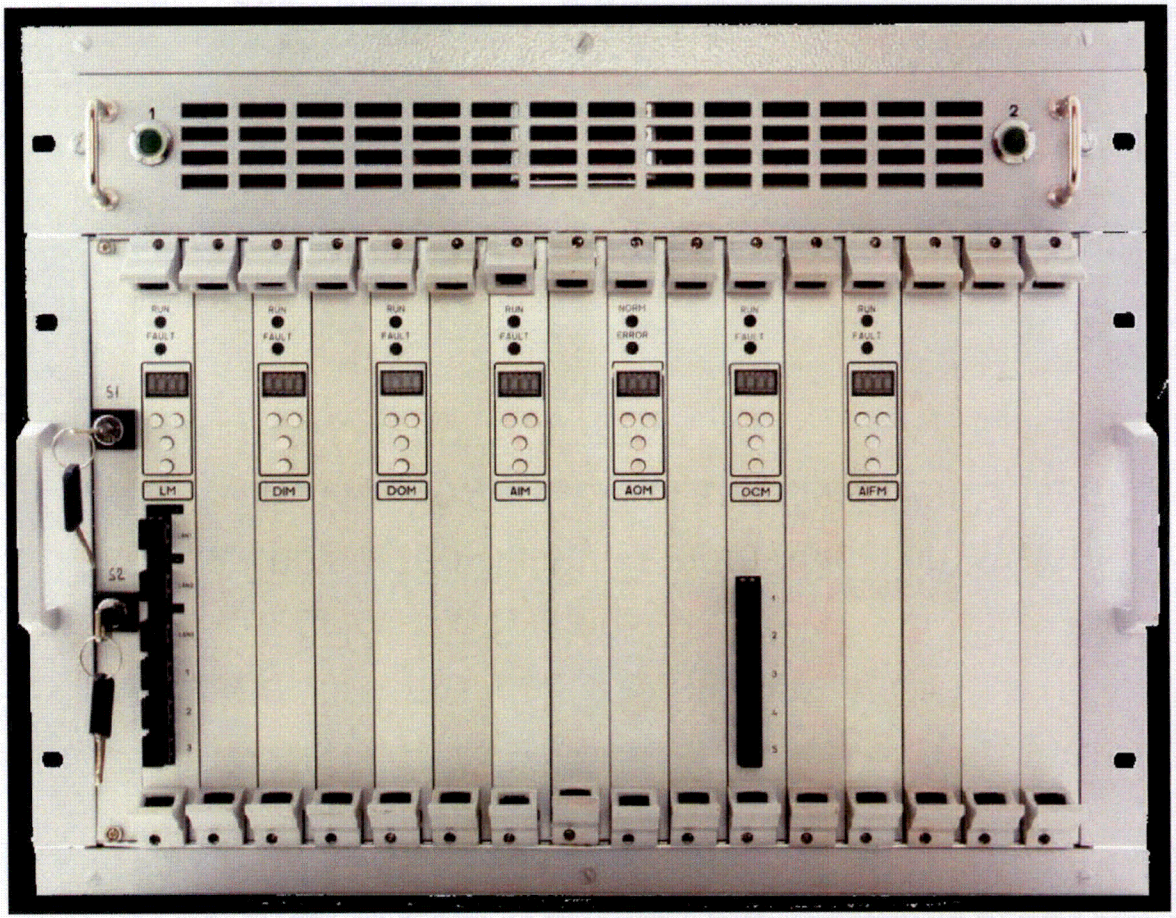
Agenda

- RadICS Platform Overview
  - Typical System Configuration
  - RadICS Platform Context
- RadICS Safety Philosophy
- RadICS Modules
- Standardized Module Electronic Design
- RadICS Module Safety Features
  - Power Supply and Watchdog Unit
  - Safety Override Unit
  - Tuning Mode Access Control
  - Communication Interfaces
  - Self-Diagnostics

RadICS Platform Overview

Product Highlights

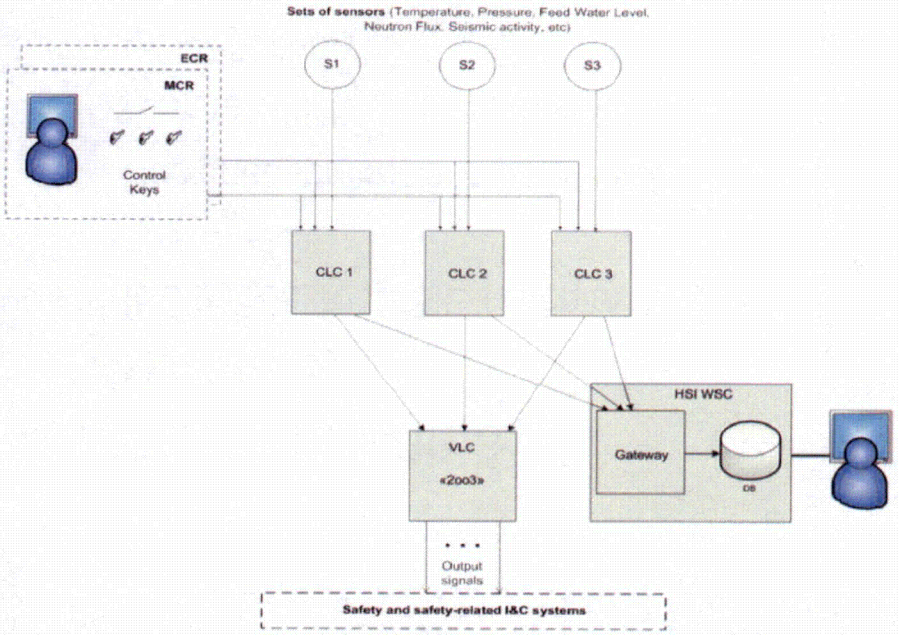
- FPGA-based
- IEC 61508:2010 SIL 3 architecture (in one chassis)
- Designed for Nuclear Safety I&C
- High reliability, functional safety and cyber-security
- Comprehensive, tried-and-tested I/Os
- Flexible redundancy management
- Comprehensive on-line diagnostic
- Fast response time (5 ms)
- Hot-swapping of modules (if needed)
- High resistance to external impacts



Typical System Configurations

Configuration Flexibility:

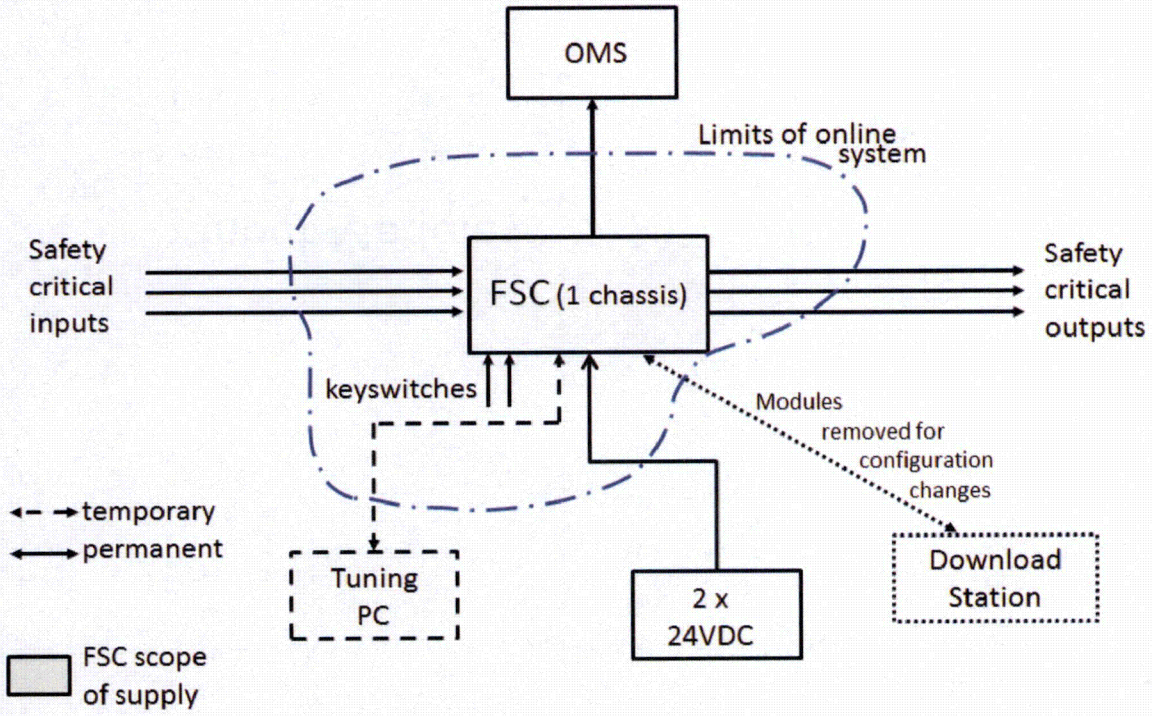
- 2, 3, or 4 channel systems
- Separate trip processing and voting layers



Used for Safety I&C Systems:

- Reactor Trip System
- Engineered Safety Feature Actuation System
- Reactor Power Control and Limitation System
- Rod Control System

RadICS Platform Context



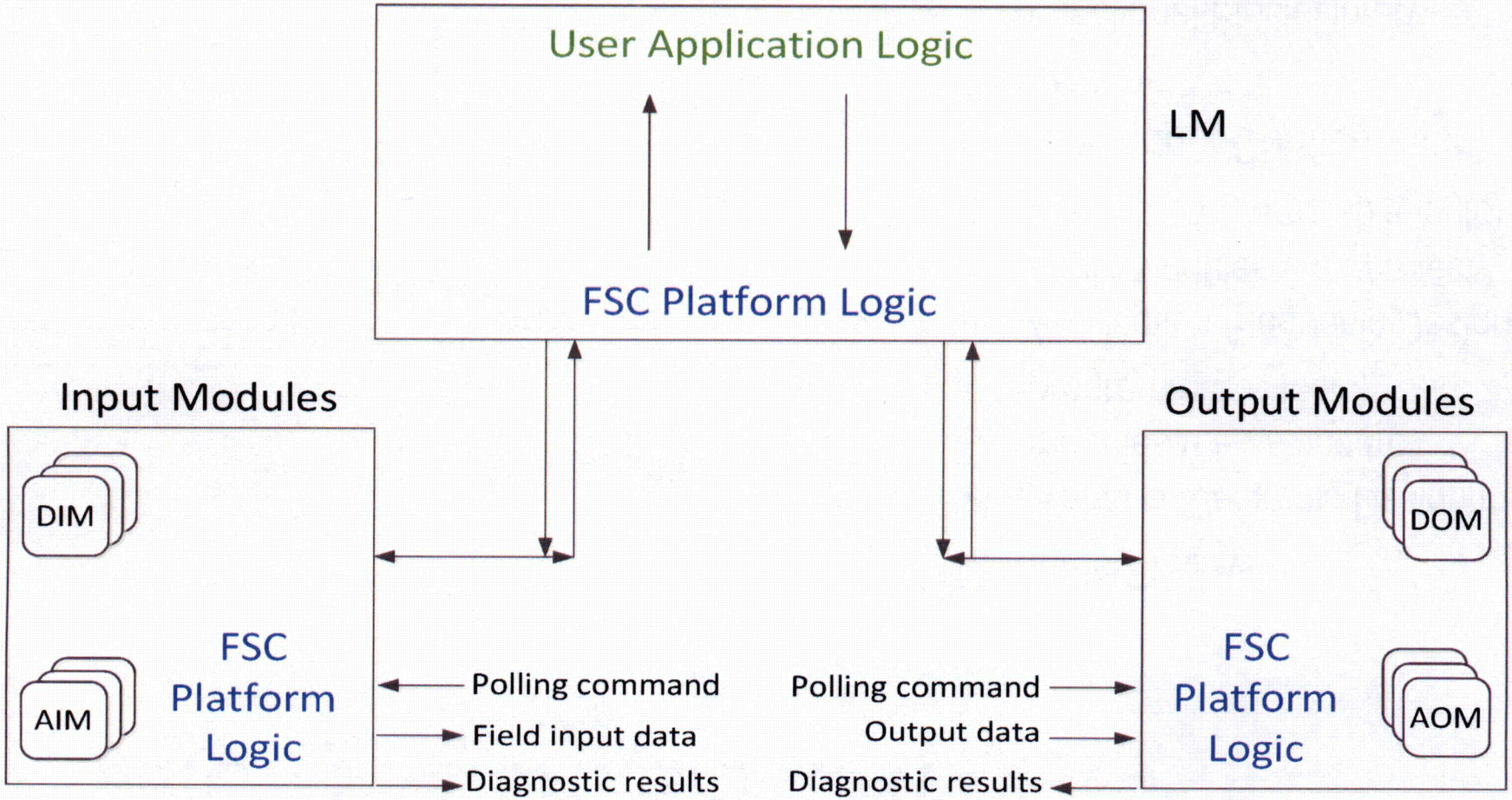
Modules FPGAs:

- Platform Electronic Design for all modules (i.e., standard programmable logic)
- Application Electronic Design for Logic Modules (i.e., project-specific programmable logic)

Radiy Product Configuration Toolset:

- Functional Block Library
- Separate libraries for platform and application

RadICS Platform Architecture



RadICS Safety Philosophy

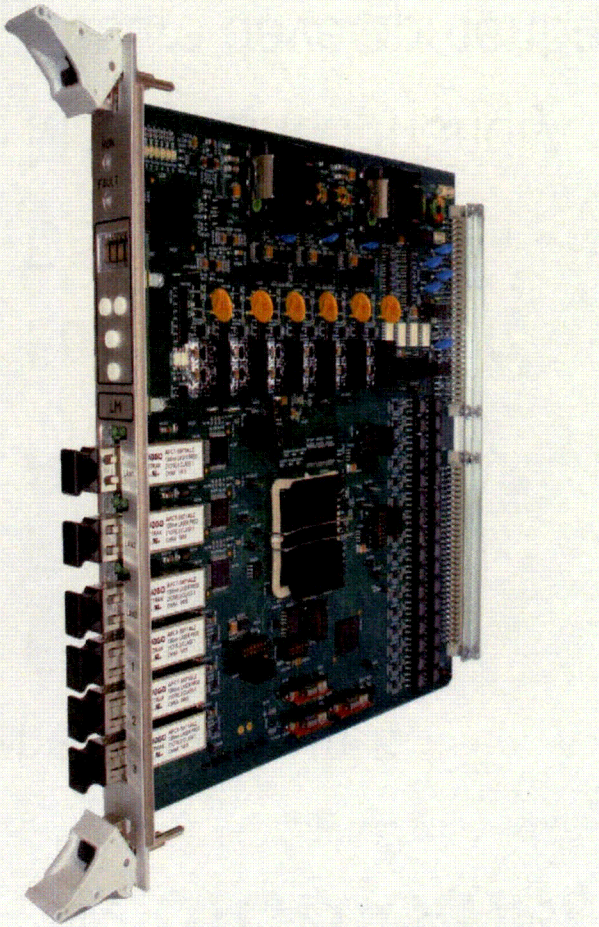
General Attributes

- Fail-safe
- Fault-tolerance
- Diversity capability
- Functional isolation
- Determinism
- Maintenance friendly
- Secure development and operational environment

Fundamental Safety Approach

- De-energize to trip
- Automatic Transitions to the Safe State
- Human Action to Leave the Safe State
- Safety Modules Only
- IEC SIL 3 Capacity by Design
- Controlled Scope and Interfaces

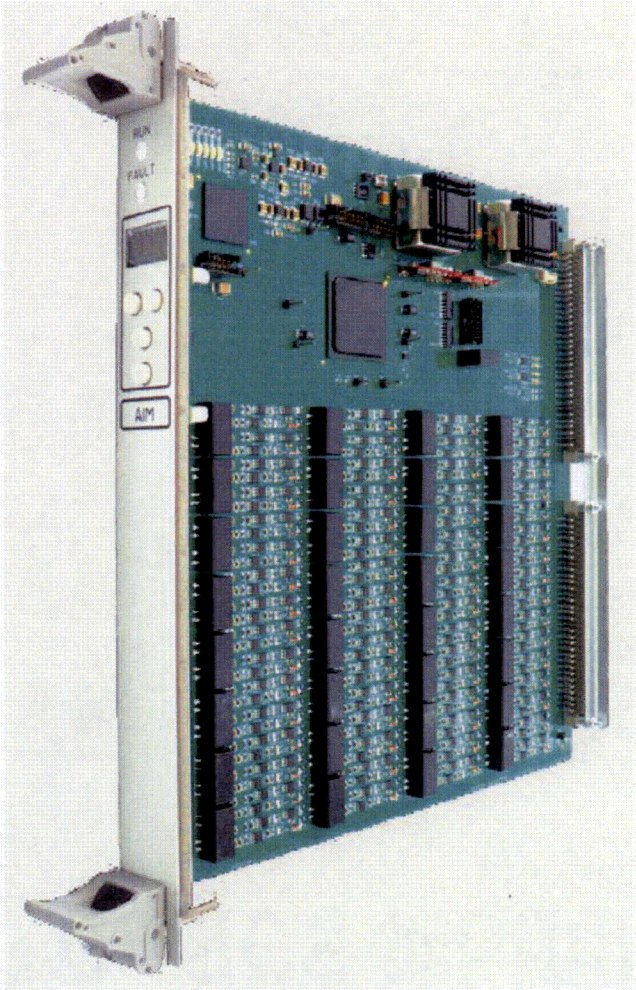
RadICS Modules (1/7)



Logic Module (LM)

- Dedicated SRAM FPGA chip for user configurable control logic
- Integrity checks on each communication line
- 14 LVDS full duplex lines for communication with OCM and I/O modules
- 3 galvanic-isolated discrete inputs (2 available, 1 reserved)
- 6 fast discrete outputs with embedded diagnostics of the outputs state
- 3 fiber optical lines for internal system communications
- 1 input for Tuning PC programming access key signal
- 3 Fast Ethernet (100 BASE-FX) optical communication lines
- Hot swappable

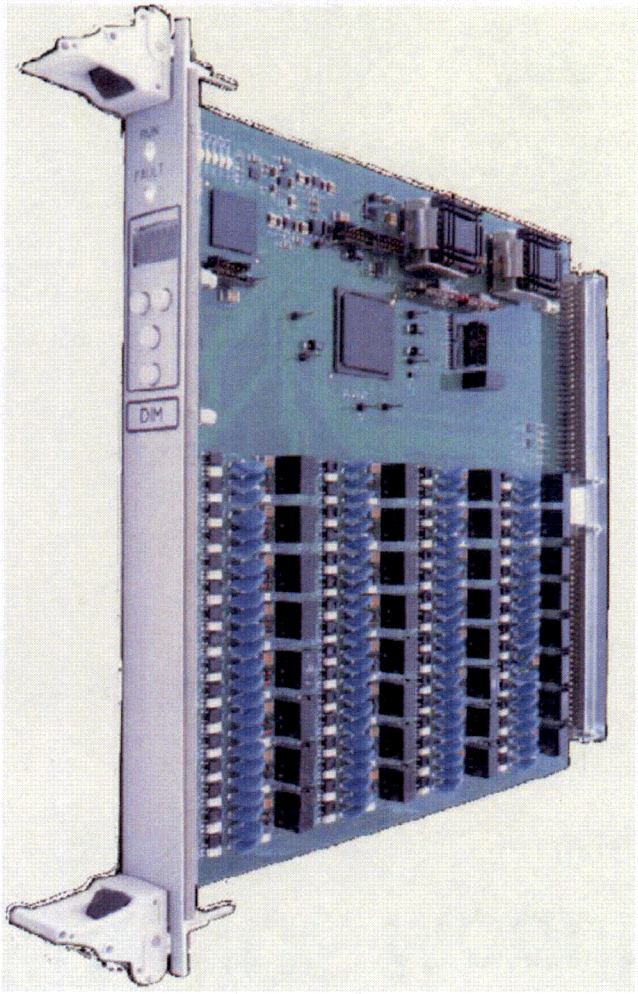
RadICS Modules (2/7)



Analog Input Module (AIM)

- Enhanced I/O diagnostics
- 32 independent analog input channels
- 18-bit analog/digital (A/D)conversion in each analog input channel
- 2 Low-Voltage Differential Signaling (LVDS) full duplex lines (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Built-in calibration
- Hot swappable

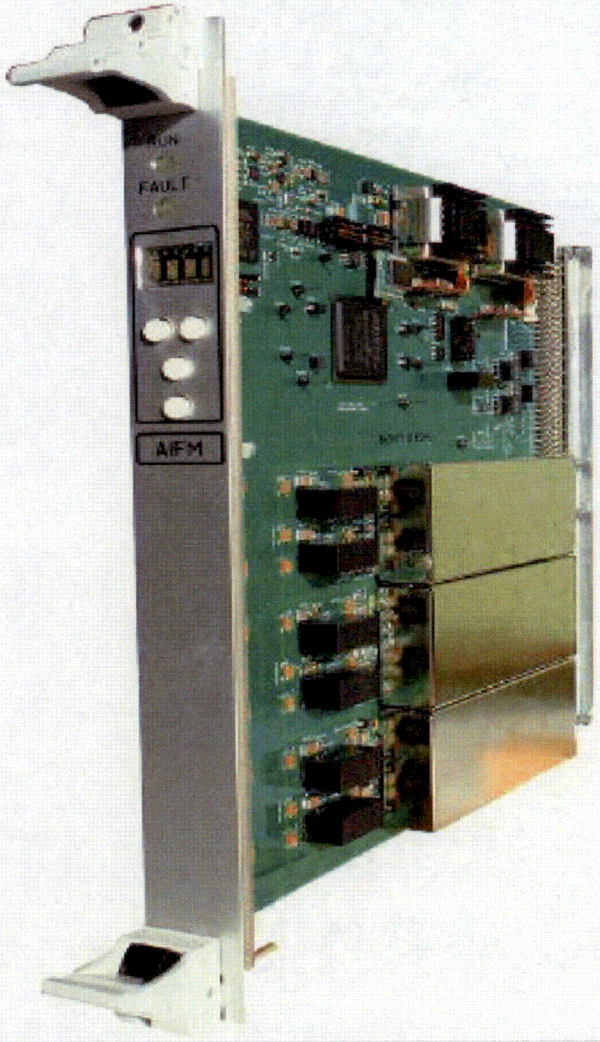
RadICS Modules (3/7)



Digital Input Module (DIM)

- Enhanced input diagnostics
- 32 independent discrete input channels ("dry" contact type)
- 2 LVDS (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Hot swappable

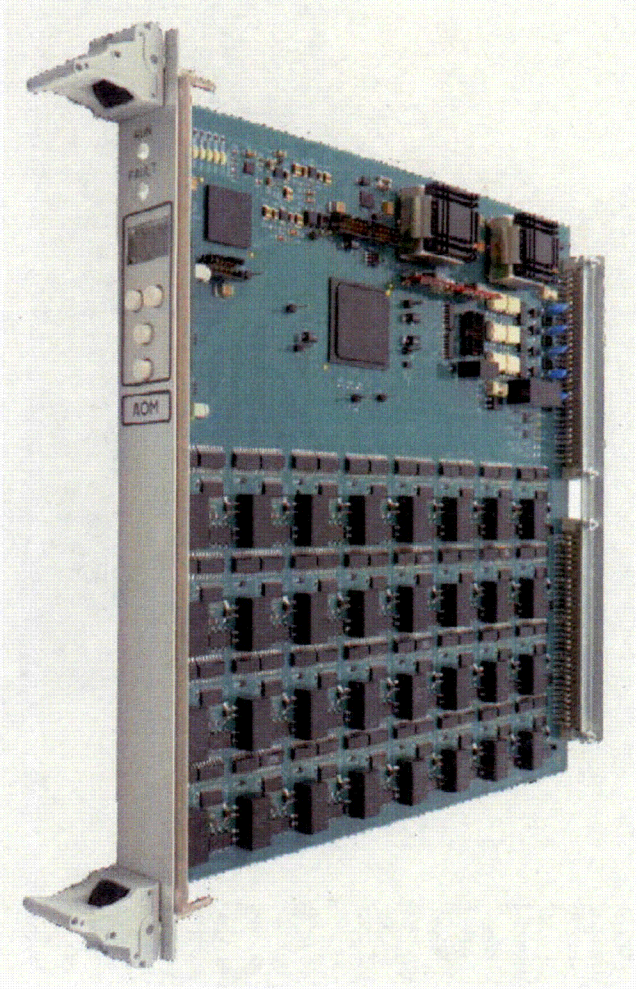
RadICS Modules (4/7)



Analog Input for (Neutron) Flux Measure Module (AIFM)

- 3 high-sensitive independent galvanic-isolated analog input channels with counting, fluctuation, or current mode
- Enhanced I/O diagnostics
- 2 Low-Voltage Differential Signaling (LVDS) lines (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Flux period calculation, flux reactivity calculation, flux power calculation
- Hot swappable
- Built-in autocalibration

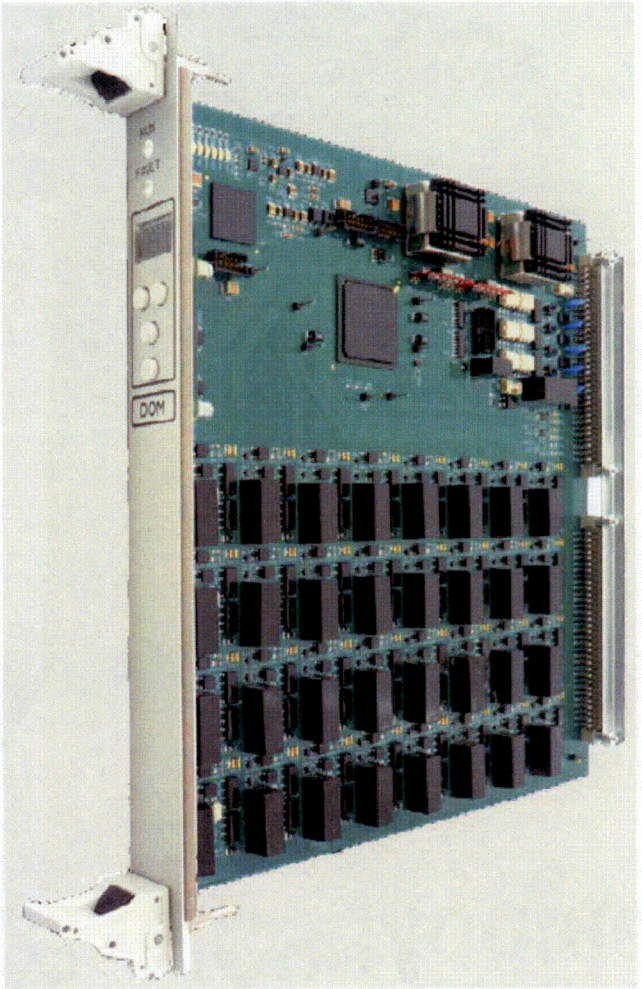
RadICS Modules (5/7)



Analog Output Module (AOM)

- Enhanced diagnostics of output channels
- 32 independent analogoutput channels
- 16-bit analog/digital (A/D)conversion in each channel
- 2 Low-Voltage Differential Signaling (LVDS) full duplex lines (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Built-in calibration
- Hot swappable

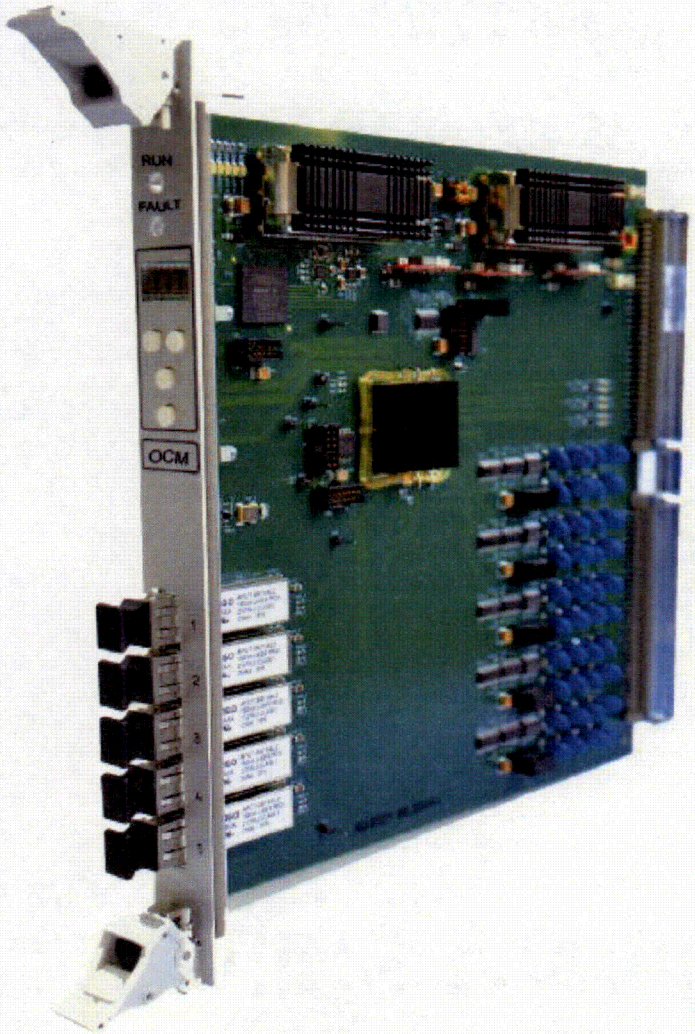
RadICS Modules (6/7)



Digital Output Module (DOM)

- Enhanced active output diagnostics
- 32 independent digital form-A optic-relay isolated output channels (switching up to 48 V DC / 0.5 amp)
- 2 Low-Voltage Differential Signaling (LVDS) (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Fuse and Overvoltage protected outputs
- Hot swappable

RadICS Modules (7/7)



Optical Communication Module (OCM)

- 5 fiber optical lines
- 2 Low-Voltage Differential Signaling (LVDS) lines (redundant diagnostic and control data exchange)
- Integrity checks on each communication line
- Hot swappable
- 5 RS-232 or RS-485 serial communication interfaces

Standardized Module Electronic Design

Electronic Design Architecture

Standardized Module Electronic Design

Modes of Operation

Standardized Module Electronic Design

RadICS Module Safety Features

RadICS Module Safety Features

RadICS Module Safety Features

Tuning Mode Access Control

RadICS Module Safety Features

Communications

RadICS Module Safety Features

Communications

RadICS Module Safety Features

RadICS Module Safety Features

- Module Fault Handling

- System Fault Handling

RadICS Module Safety Features

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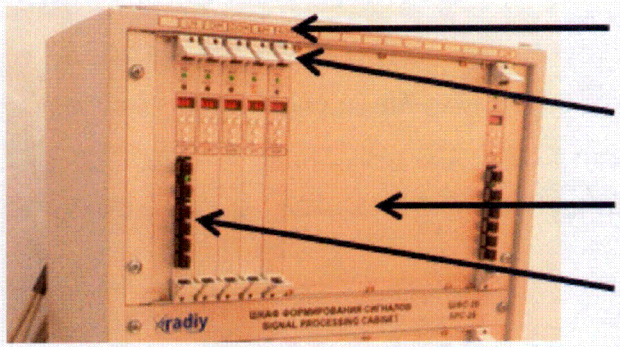
Maintenance Friendly Features



- Full insertion and complete clamp-down are visually verifiable
- All I/O cables are rear-connected

- Non-interfering local status display on every module
- Comprehensive diagnostics relayed to OMS
- Detection of some maintenance errors (e.g., wrong module in a slot)
- Hot-swap capability
- Validated maintenance documentation
- User Safety-Override

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Thank you for your attention!

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